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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

AMENDMENT "B" ACCOMPANYING FILING OF RCE

APPLICANT: Peter POST et al CONFIRMATION NO.: 5081  
SERIAL NO.: 09/522,619 GROUP ART UNIT: 3629  
FILED: March 10, 2000 EXAMINER: Naresh Vig  
TITLE: "METHOD FOR PROTECTING A SECURITY MODULE AND ARRANGEMENT FOR THE IMPLEMENTATION OF THE METHOD"

Assistant Commissioner for Patents,  
Washington, D.C. 20231

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APR 22 2003

SIR:

GROUP 3600

In response to the Office Action dated January 13, 2003, Applicants herewith amend the application as follows.

IN THE CLAIMS

Claim 1 has been amended as follows:

1. (Twice amended) A method for protecting a security module comprising the steps of:

storing security relevant data in a non-volatile memory of a security module and inserting said security module in a device motherboard;

monitoring proper insertion of said security module on said device motherboard with a first function unit and a second function unit in said security module;

signaling, at least one security-related status of said security module with said

first function unit;

detecting, at least one of improper use and improper replacement of said security module with said second function unit and, upon a detection of at least one